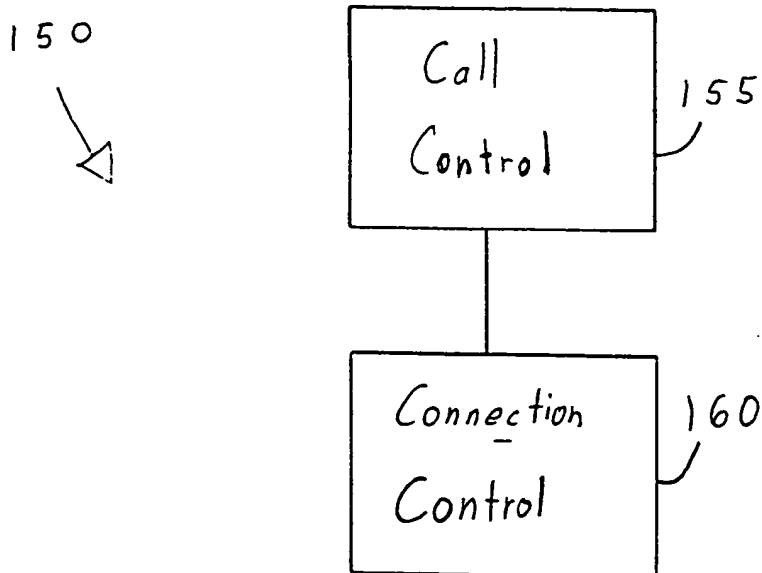


Prior Art

FIG. 1A



Prior Art

FIG. 1B

FIG. 1A

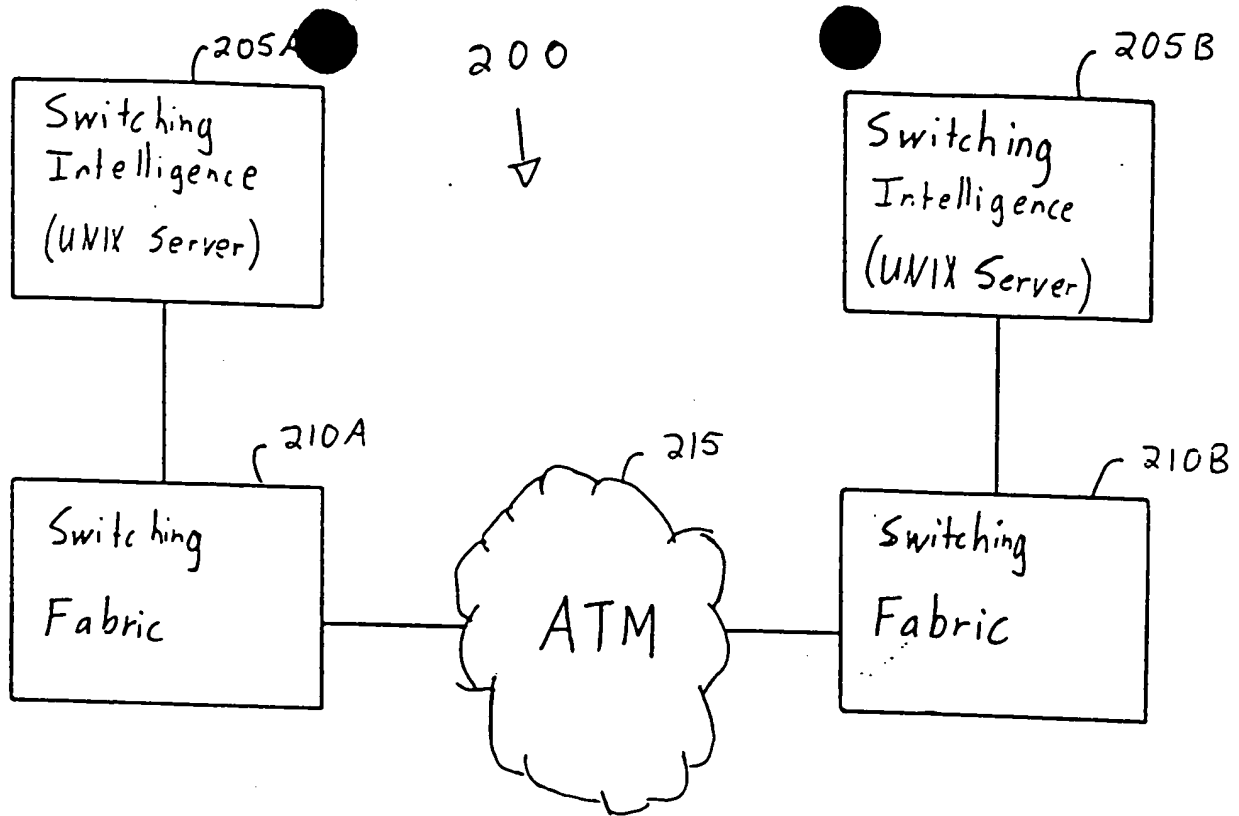
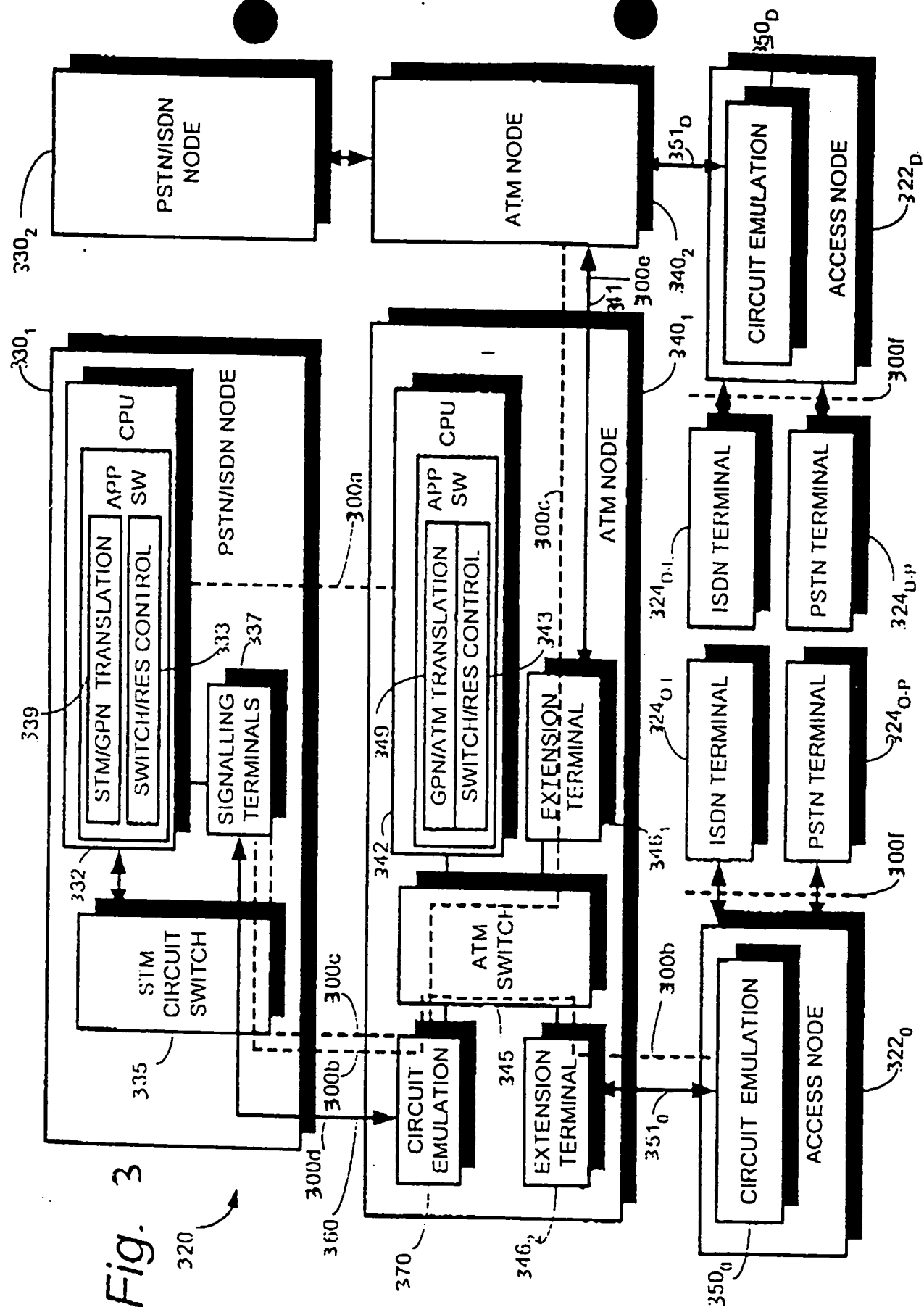


FIG. 2 Prior Art

Fig. 3



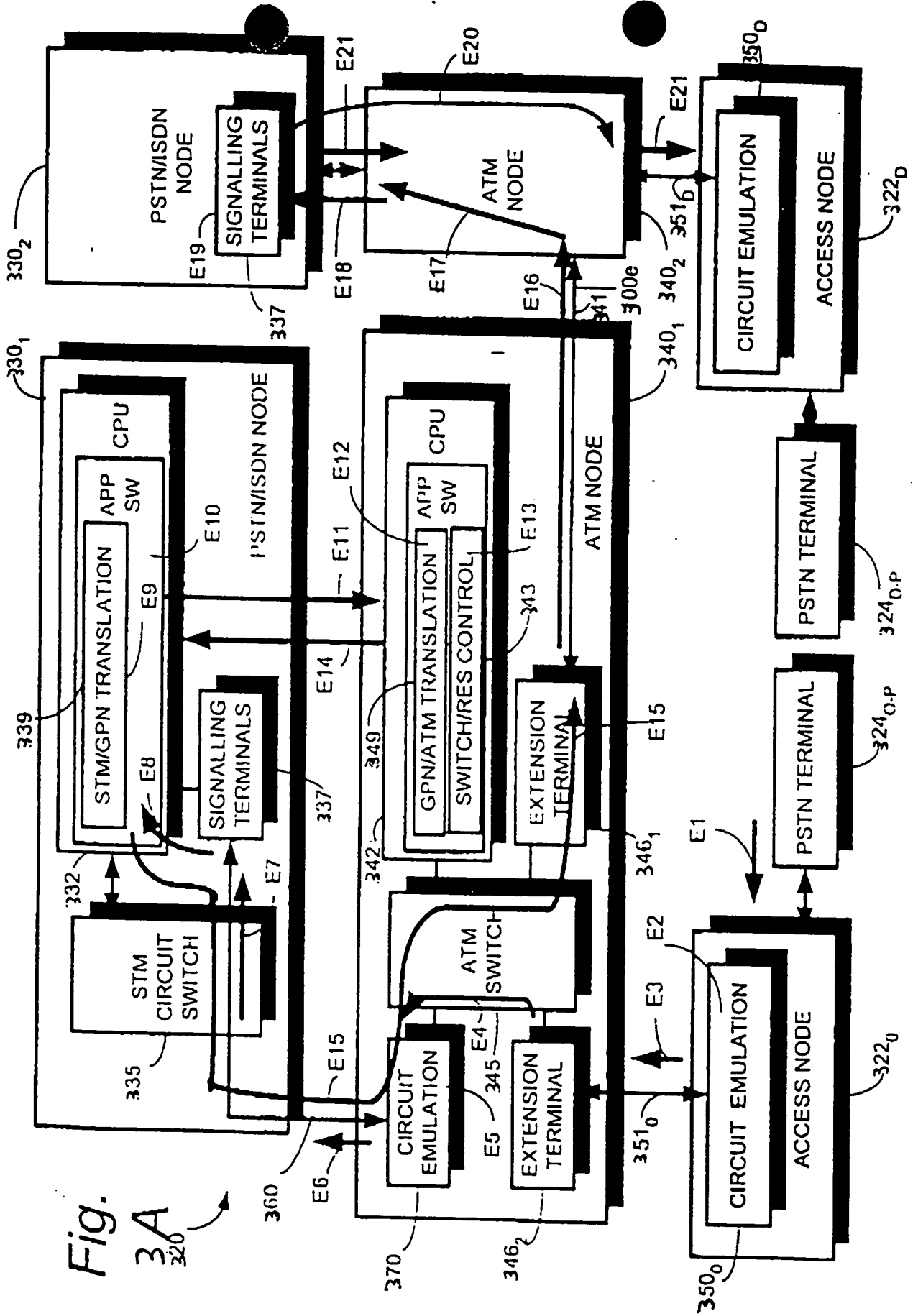
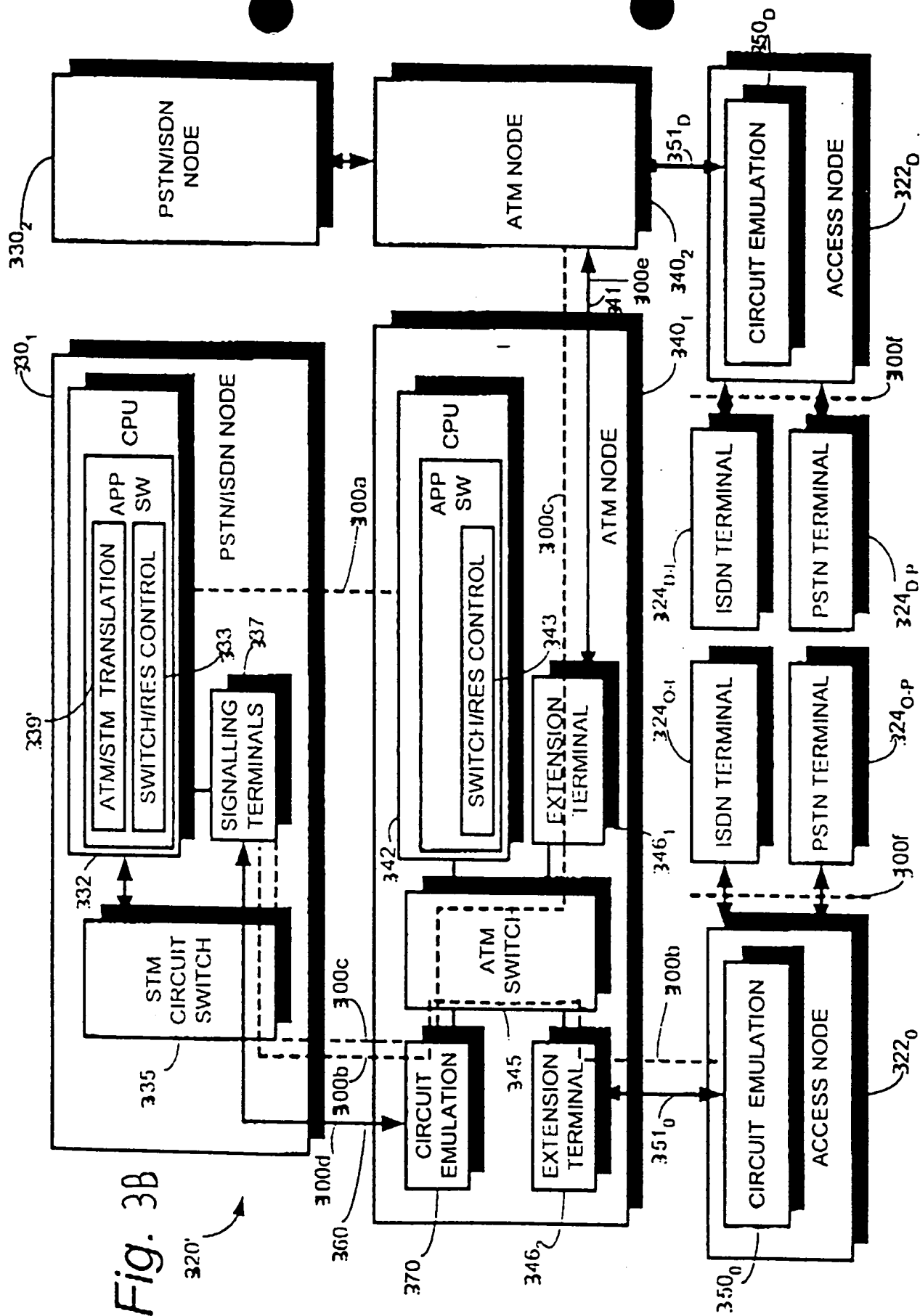
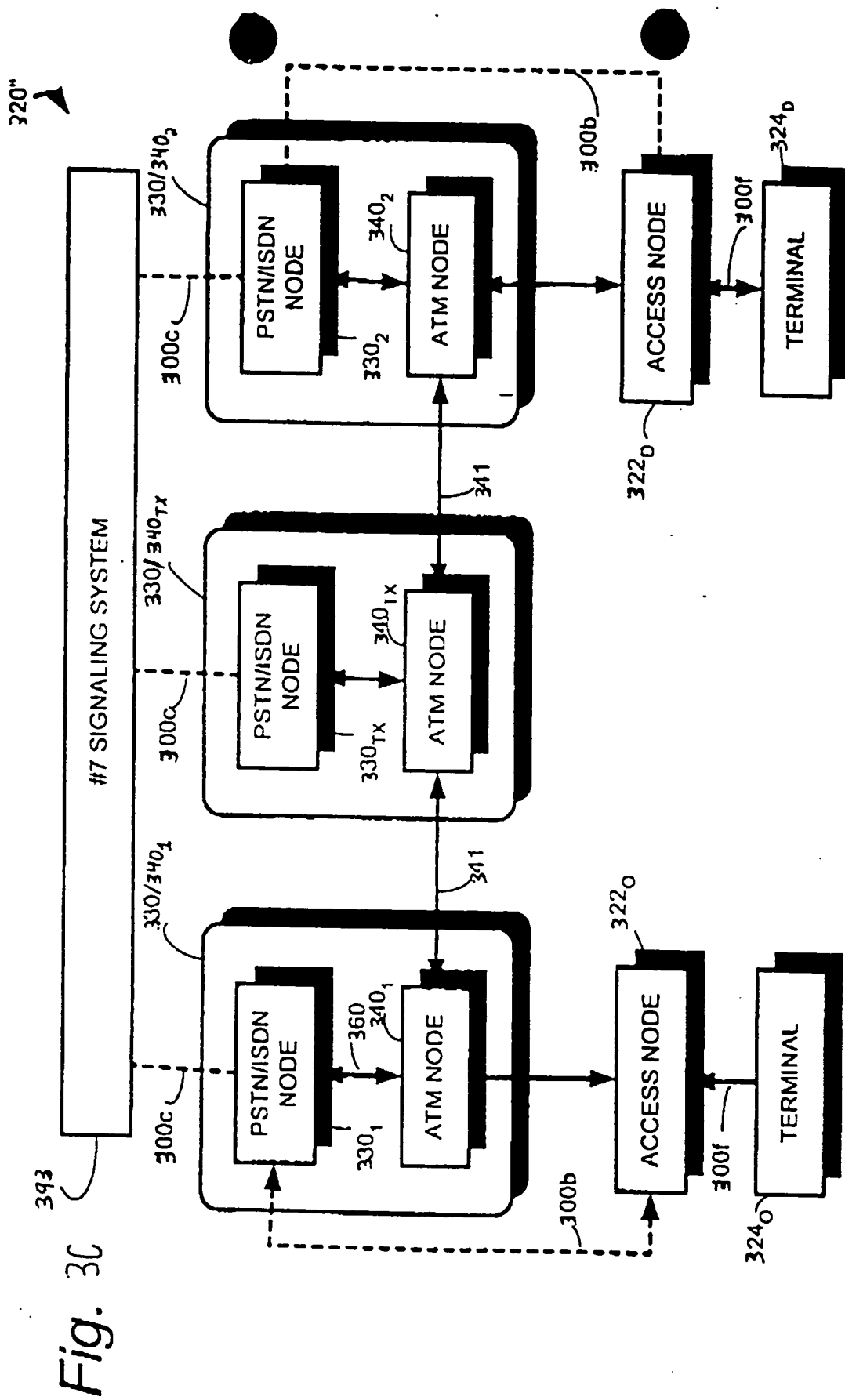


Fig.

3A





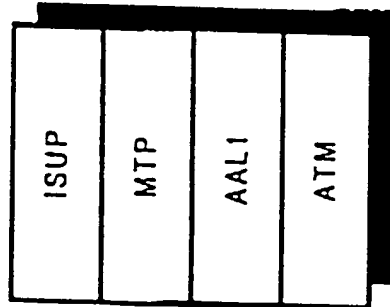


Fig. 3D

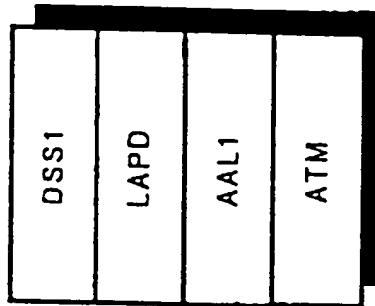


Fig. 3E

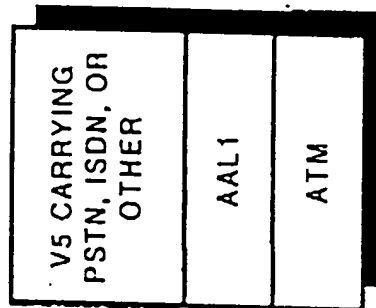


Fig. 3F

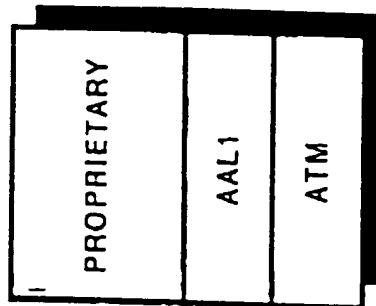
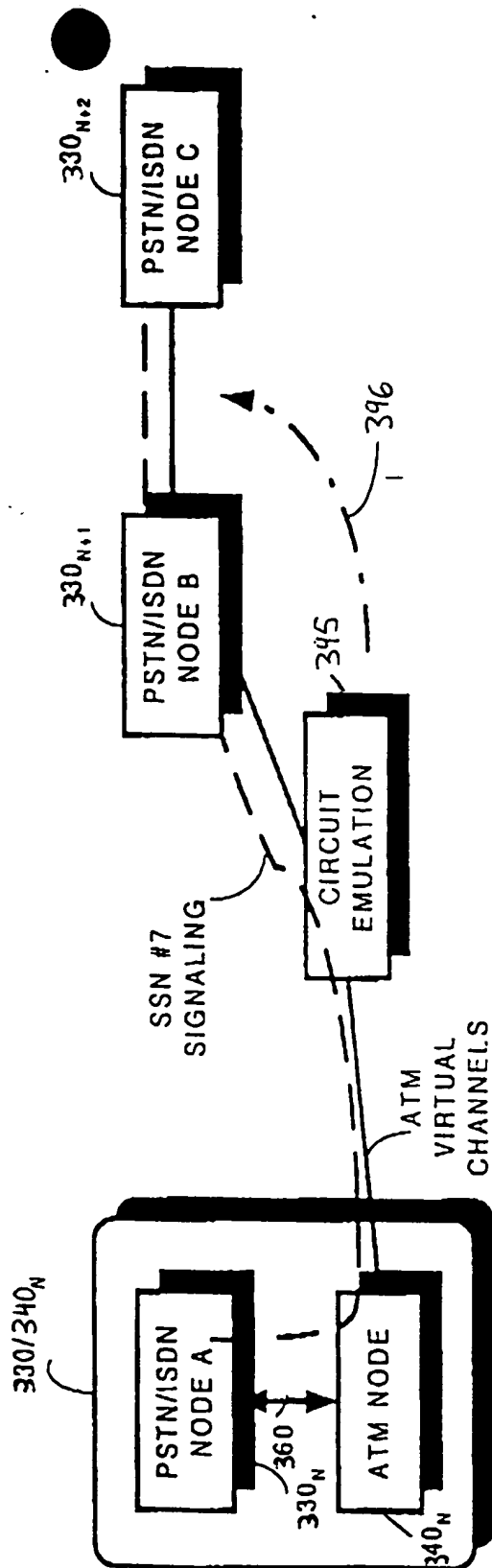


Fig. 3G

Fig. 3H



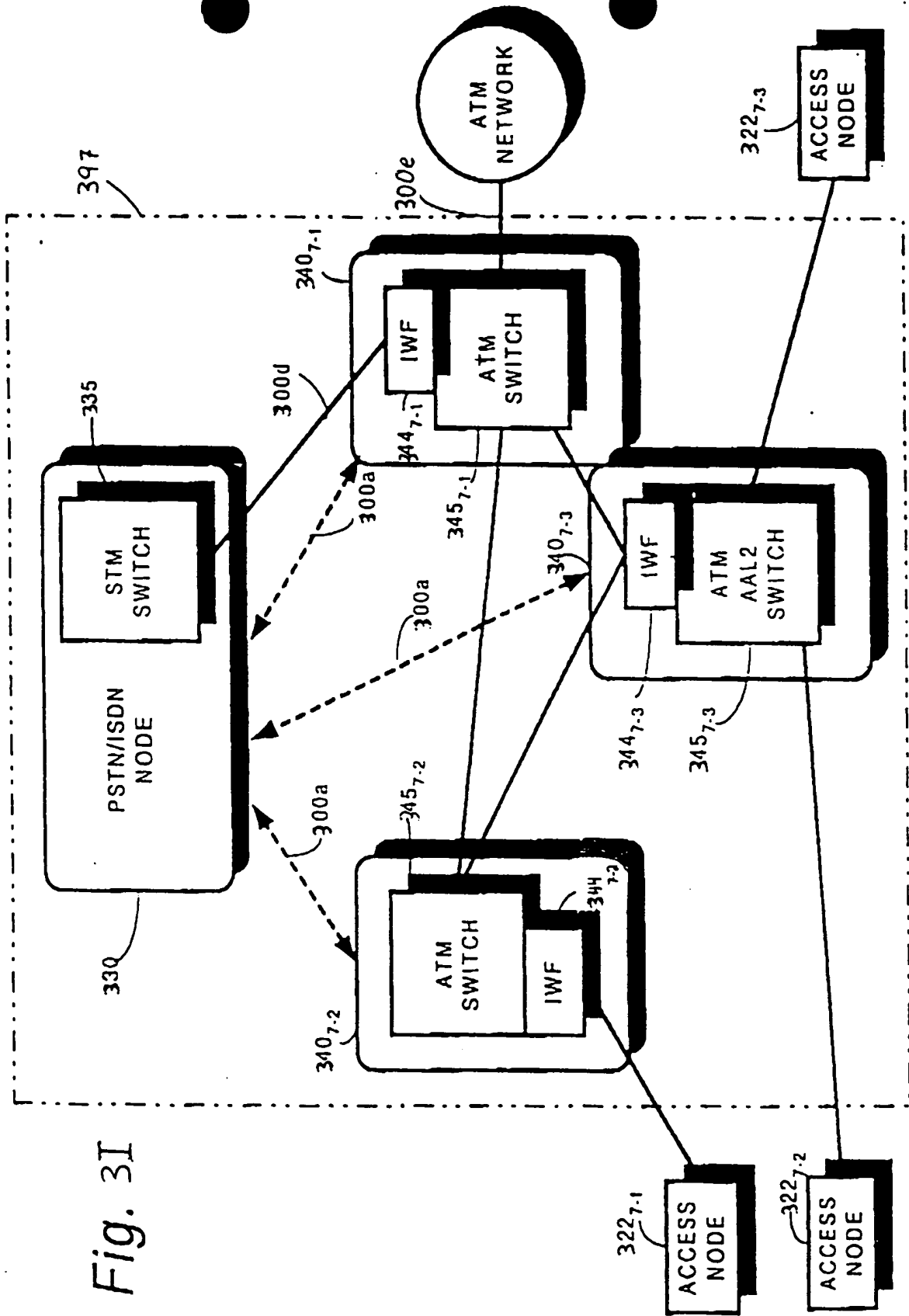


Fig. 3I

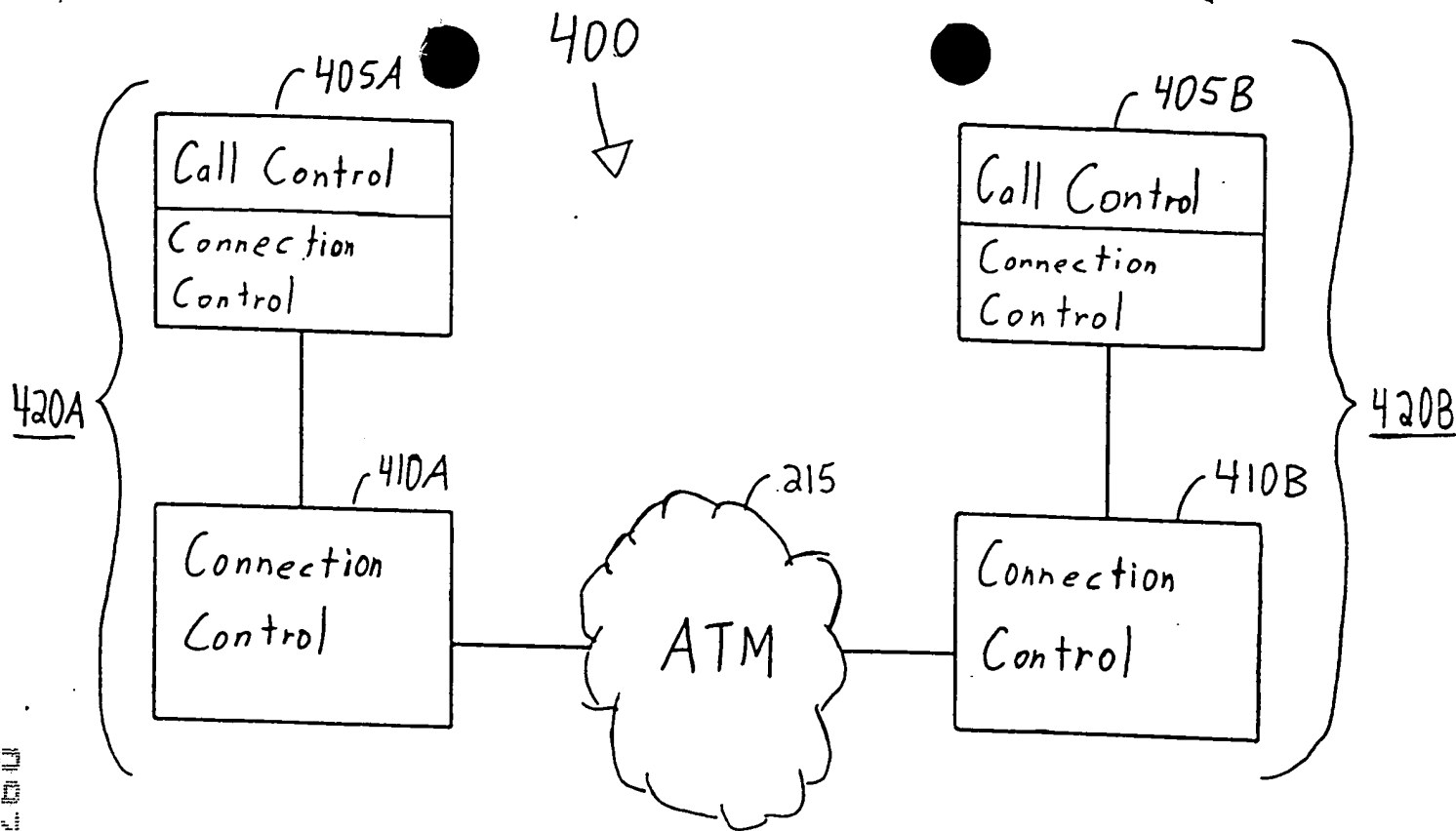


FIG. 4

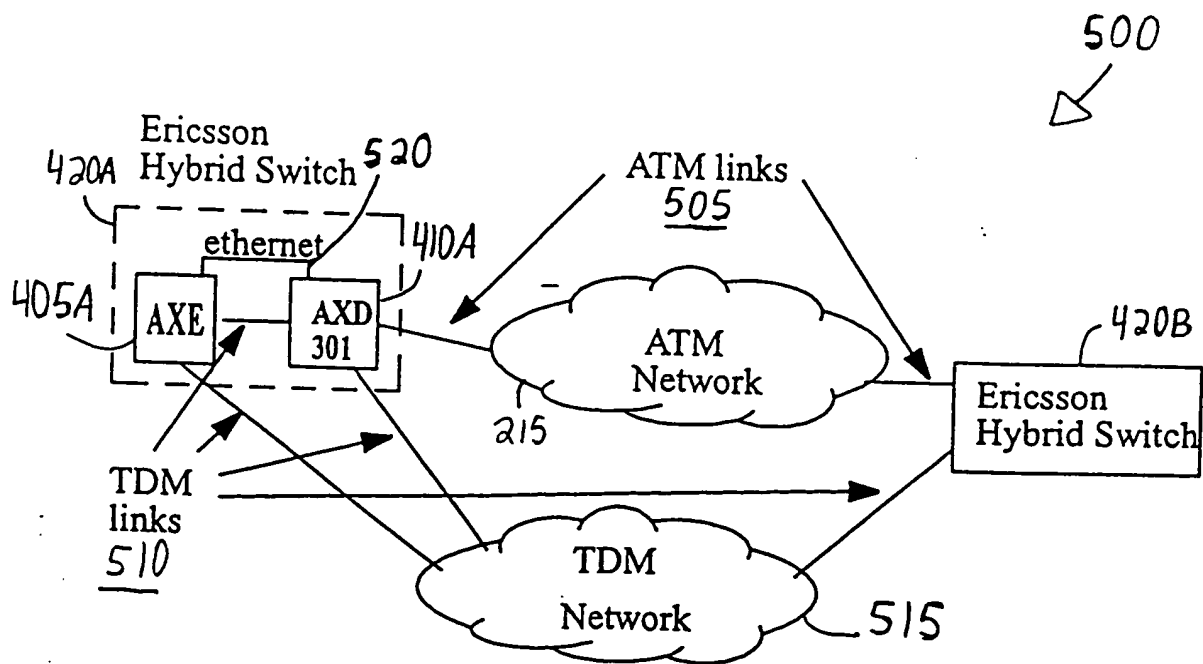


FIG. 5

Hybrid Switch

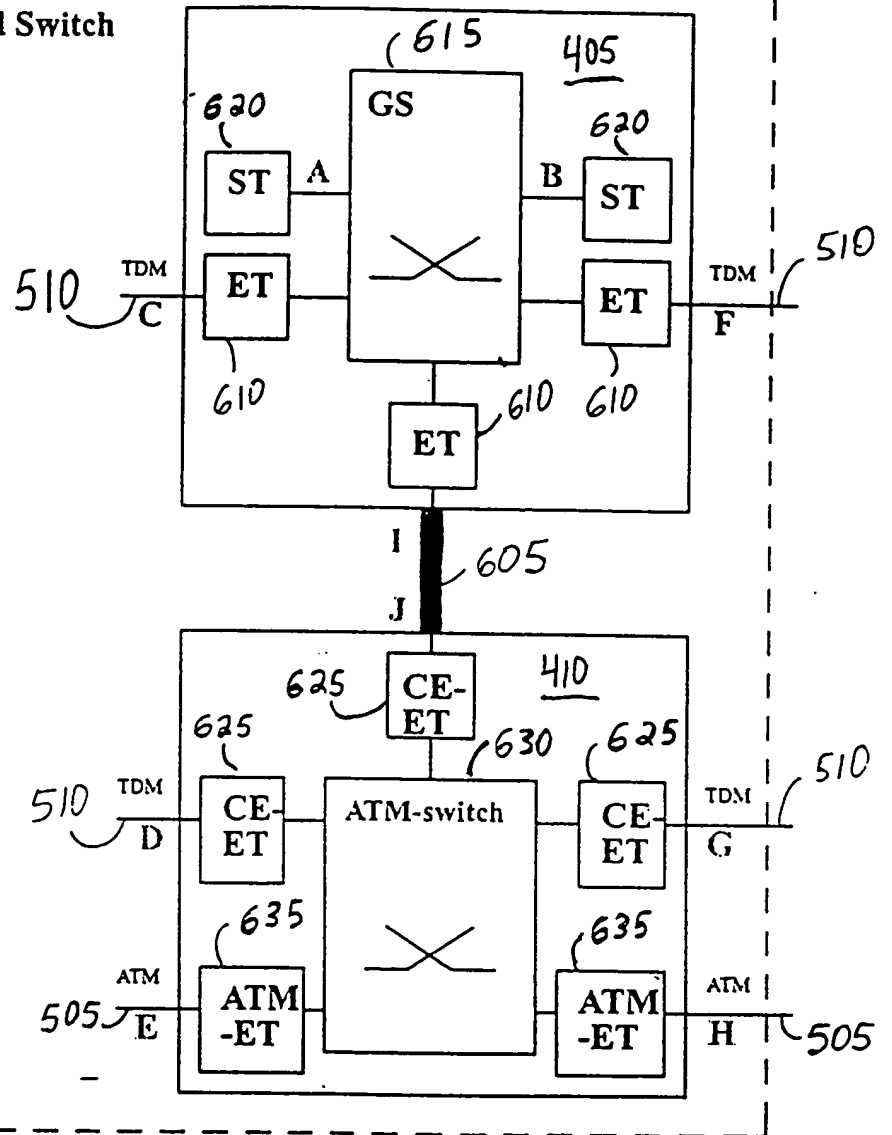


FIG. 6

700

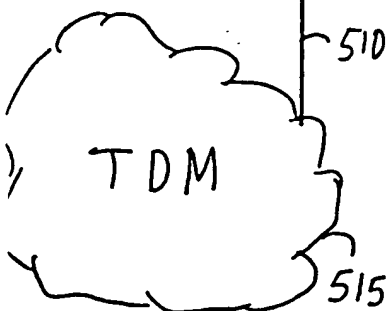
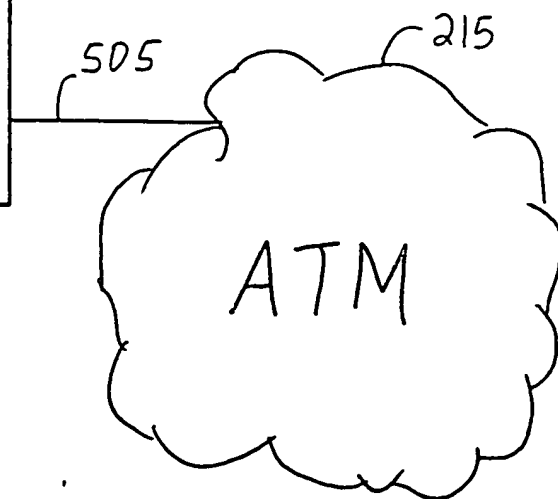
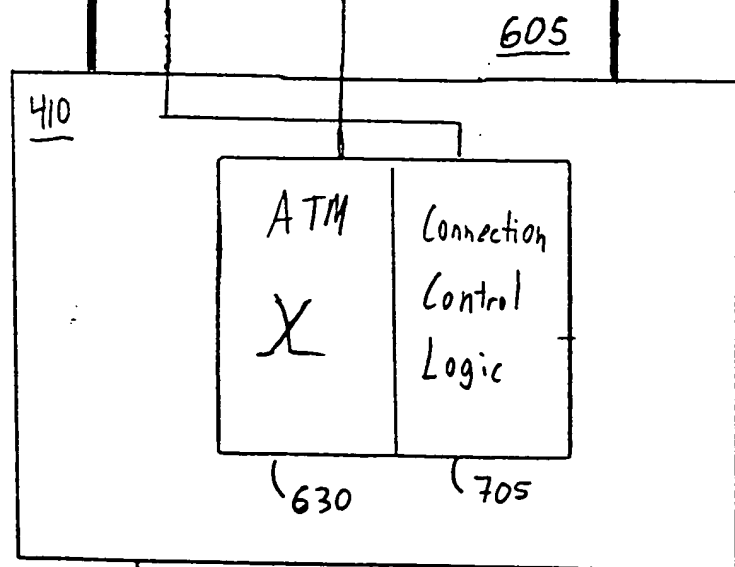
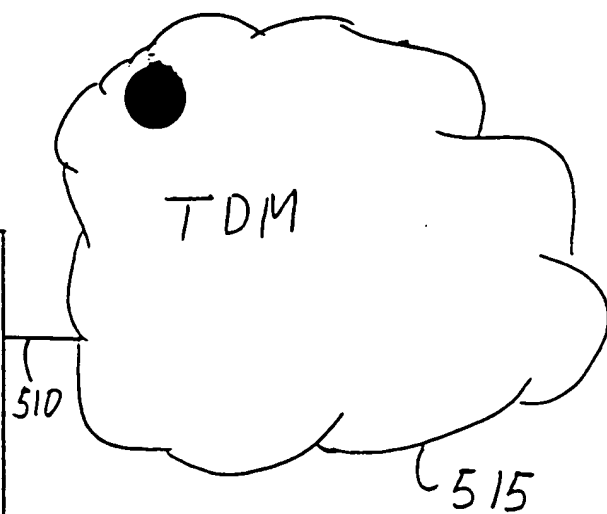
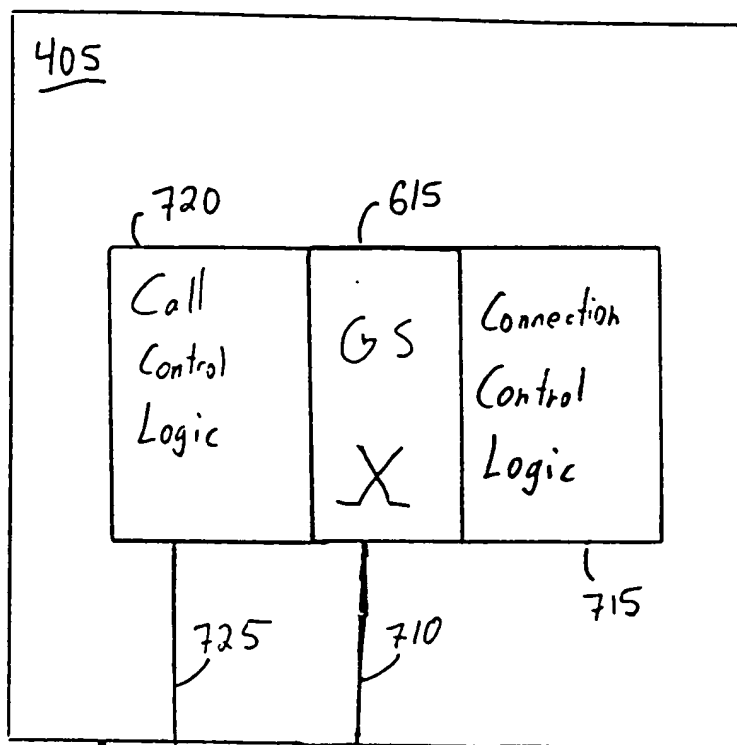


FIG. 7

0075546-04404

800

815

IN
node

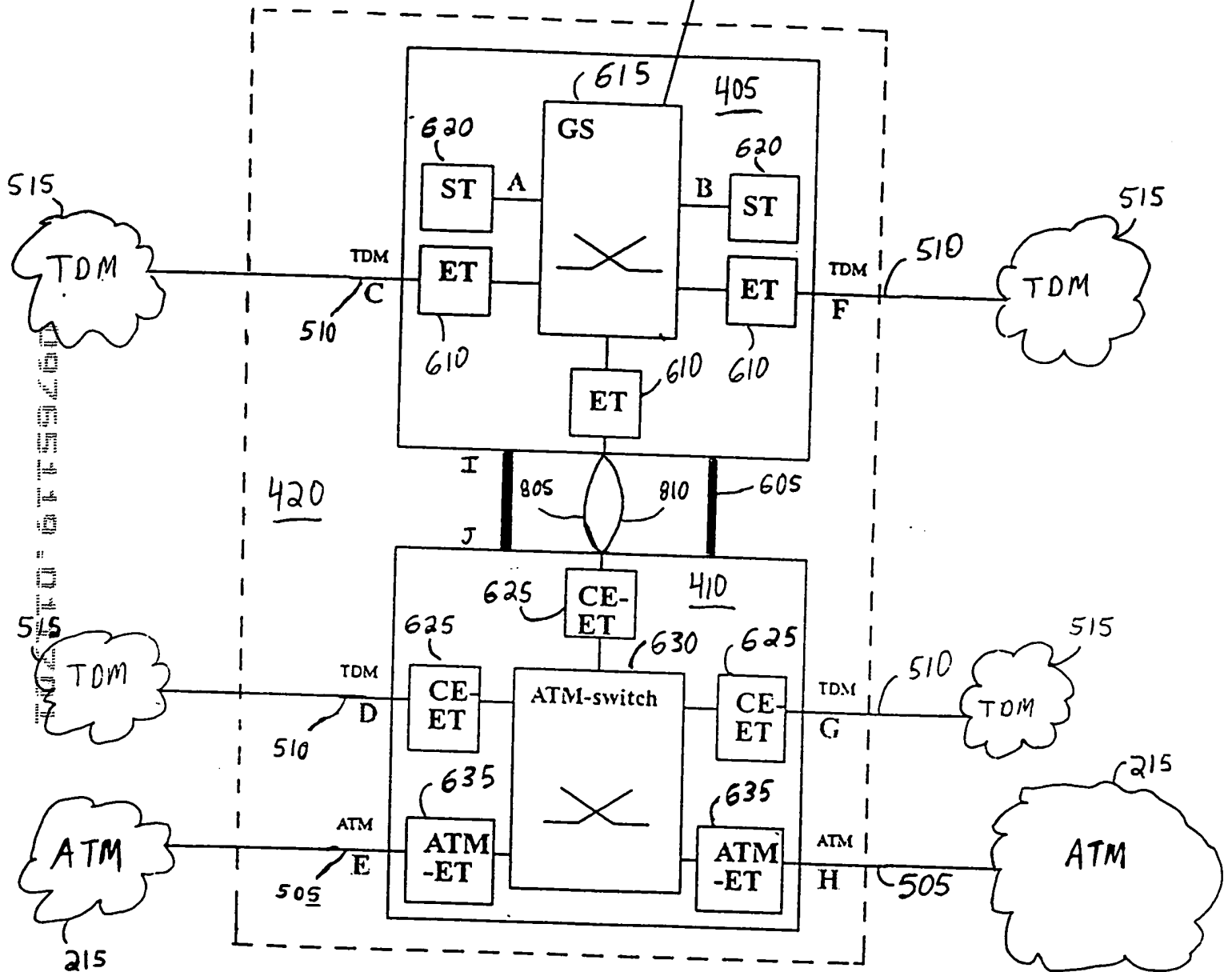


FIG. 8

900

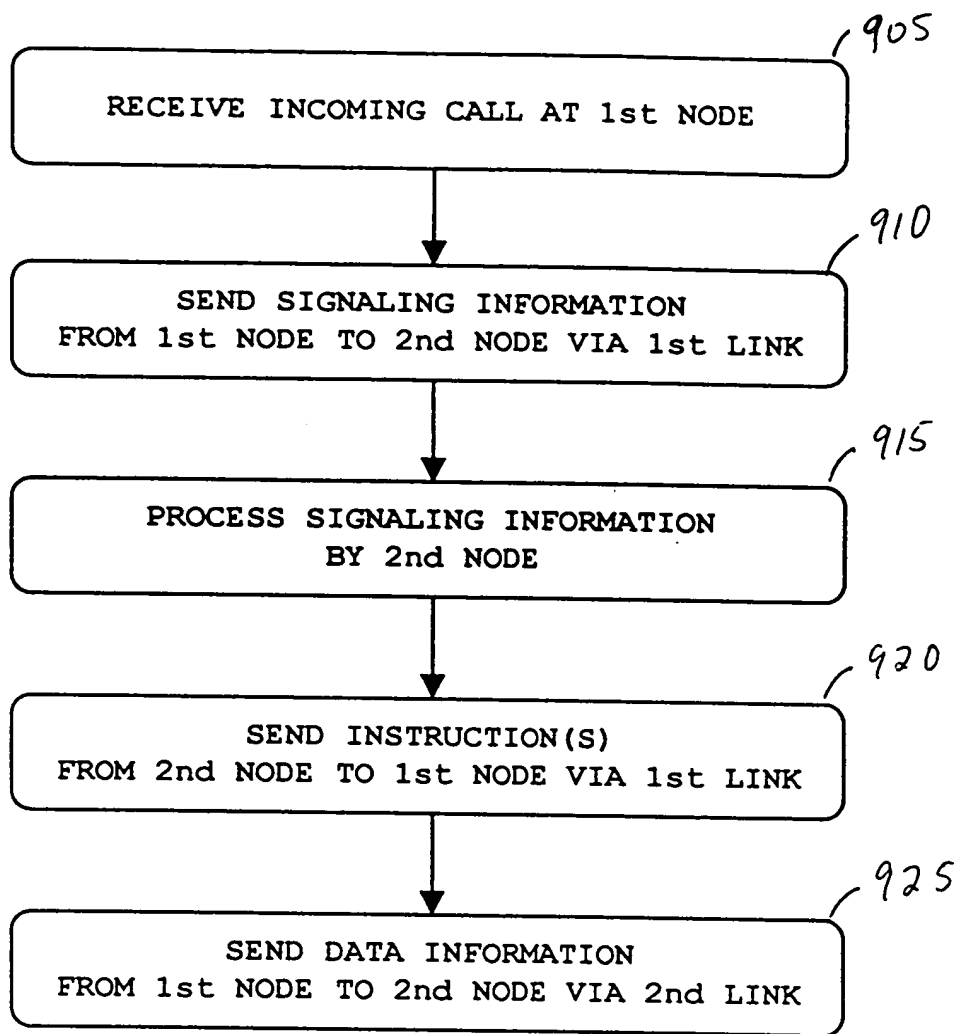


FIG. 9